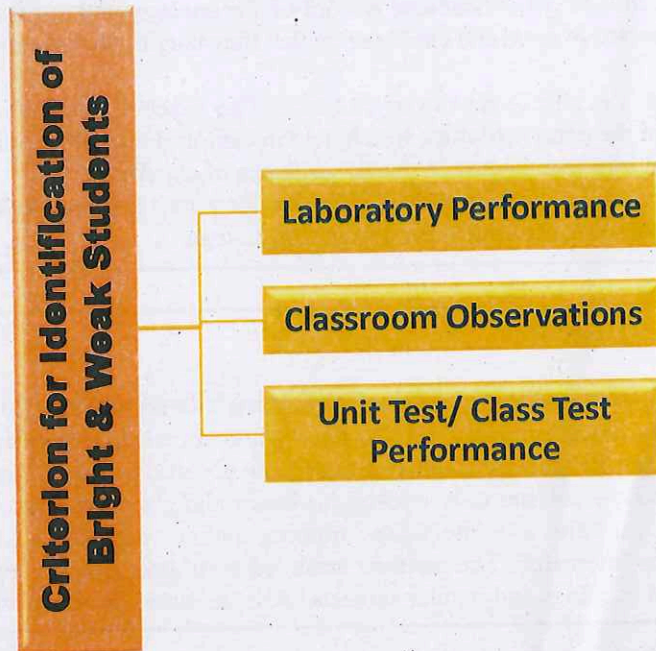


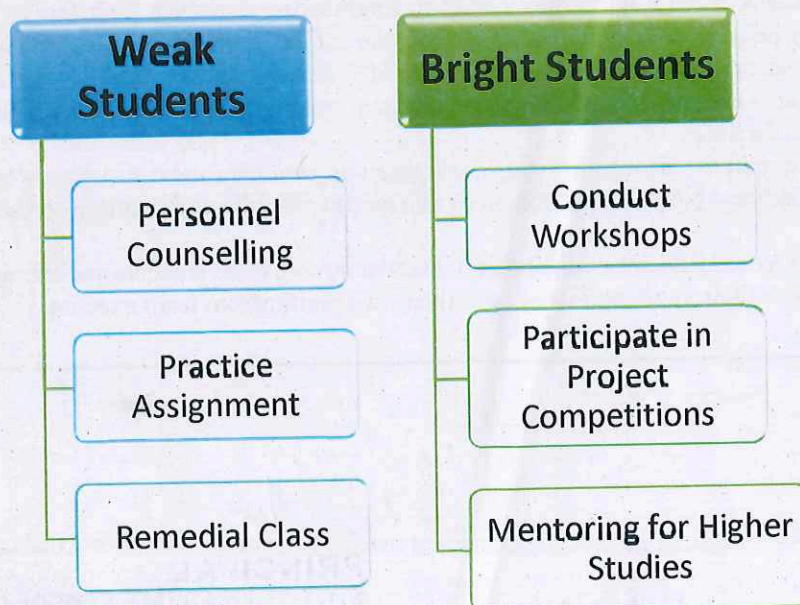
3


2.2.1: The institution assesses the learning levels of the students and organises special Programmes for advanced learners and slow learners.

The process adopted for identification of bright and weak students:



Initiative for Bright & Weak Students:




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Some of the Students Testimony (Weak Students):

Mr. Akshay Tiwari (2018-22 PPT)

Myself Akshay Tiwari from SE, PPT. I was a drop out student in first year itself. As I was weak in studies, Non of my subjects were cleared.

But over this all our college SIES Graduate School of Technology and the staff help me out clear my subjects. They are so good and encouraging that they taught me daily and help me clear my subjects.

Because of the college, I'm able to see the bright future. The Teachers and staff always support for being better me and the extra activities, which held in campus helped me to find my passion about photography and improve them with proper guidance of experts.

College not only help me being good in studies but also becoming a good person for our society. I'm very thankful for pursuing my engineering from this college.

Mathivanan Roll No.: 116A2048

Mathivanan took admission in 2016-17 for the EXTC branch. In second year 2017-18 he found difficulties in core electronics subjects as he was having less interest in engineering and wanted to make his career in the finance area. For two consecutive years, he got a year drop. During these two year, the mentor continuously tracked his study and also suggested him to use this period for some extra activities like internship / training/ online courses. As a result he could complete some courses/Internship. The institute arranged extra practical sessions for practice. He has now cleared all backlogs and regular subjects and is in third year and is doing well.

Aman Rajput (2015-19 EXTC)

I was very blank when I started my career in Engineering, however I am forever grateful to SIESGST for helping me out in choosing a suitable career. It not only paved a path for me but also served several opportunities, considering it's flexible environment in academics and extracurricular. Of course, none of this could have been accomplished without the immense support of the facilities.

College also conducts various workshops, talk sessions, seminars and competitions from various domains in Tech and Non-Tech which gives a chance to all students to explore and advance their expertise.

SIESGST has a core students council that boosts the morale of its students and encourages them to come up with their views and ideas built their own platforms to learn execute.



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Bright Students:

Sayli Patil 2016-20 CE

Sayli Patil was a very bright student and the institute encouraged her to pursue her passion

Gst Developer Program (GDP) was the start of my journey in the technical field. Going to college on Saturday was fun because we were taught things that were not in our textbooks. I was a part of the technical team since 2nd year, a place where innovative ideas were brought into reality. The principal, the student incharge, computer department faculties always supported me. Everyone including my friends encouraged me a lot to follow my passion. If it was not SIES I wouldn't have been exposed to these opportunities. All the hard work was well appreciated by my college. It was my college that encouraged me to pursue what I was truly passionate about, I learned to multitask and never compromised academics. Thanks for all the support.

Sanjeev Srinivasan 2014-2018 PPT

Sanjeev was a very bright student but couldnot cope with Mathematics in his First year and He had year drop in third year. The institute helped him to overcome this challenge and this is what he wants to share with us-

In today's world capabilities are simply rhetorical, in a rapidly changing world the defenition of competency changes every other minute. SIES GST in many ways has prepared me for this rapidly changing environment, the focus on soft skills developed through extra curricular activities along with the hard skills expected out of an enginner has made me a confident and capable professional who can adapt and survive the volatility of the world economy

The Institute has a policy of allowing bright students to join in leading roles in the student council to groom and hone their team building and leadership skills and prepare them for a bright career

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The Institute has a policy of allowing bright students to join in leading roles in the student council to groom and hone their team building and leadership skills and prepare them for a bright career and future.

- **VISHAL DUBEY BE EXTC (2012-16) - Technical Secretary**
An entrepreneur, developing autonomous tractor has been selected as one of the 120 entrepreneurs from around the world for APIA Asia Pacific Innovation Accelerator Camp happening in Beijing, China with a 100% scholarship.
- **PRANIT KULKARNI BE Mech (2013-17) - SAE team**
Pursuing LLB with Honours in Intellectual Property Law at Indian Institute of Technology, Kharagpur.
- **CHAITANYA BHAVE BE Computer Engg (2013-17) - Technical team lead**
A research scholar working on a prestigious project at University of Florida.
- **VISHAL VINOD BE Mech (2012-16) - Secretary**
Coordinator at Business Innovation -The Design Fabrication Zone (DFZ) at Ryerson University.
- **SANJEEV SRINIVASAN BE PPT(2014-18) - Design team head**
Won the Student of the Year award in PrintWeek India Awards 2018.

Some of our bright students were encouraged to go for International internships

		
<p>OMKAR BHATUSE Institute Ural Federal University, Yekaterinburg, Russia</p> <p>Projects Design of Handling Manipulator for Ring Furnace</p> <p>Duration 18 June 2018 to 26 July 2018</p>	<p>KAVYA RAJIV Institute Athens Information Technology, Greece.</p> <p>Projects Sentiment analysis using Twitter Api, RSS feed categorisation, Face detection using foreground segmentation.</p> <p>Duration 19 June 2018 to 31 July 2018</p>	<p>SWAPNIL SHINDE Institute Athens Information Technology, Greece.</p> <p>Projects Implementation of a Blockchain-based Application for Tracking the Shipment of Goods, Implementation of Smart grid using blockchain</p> <p>Duration 18 June 2018 to 31 July 2018</p>



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Special Awards instituted to encourage students with exemplary achievements:

Instituted by	For		Year & Branch
by our Honorary Advisor	Scholarship for exam fee waiver	Honey Berman	TE EXTC
		Gaurav Patel	TE CE
		Yash Zavar	TE IT
		Ananda Patil	SE PPT
		Swarup Dhanwade	TE MECH
Head of the Department of the Information Technology, Dr. Laxmi Sudha	Most Enterprising women	Teertha Shetty Sayali patil	
	Best Team	Baja Team	
Special Awards	Technical Team of SIESGST		
	C-DAC Nvidia hackathon, IIT-B's I_hack and many more	Venkatesh Arunachalam, Sayali Patil and Shantanu Ghar	
	Smart India Hackathon in addition to winning hackathons at Sardar Patel Institute of Technology and Datta Meghe College of Engineering	Rushikesh Shinde, Akhil Nair, Sunil Pawar and Kush Shah	
	ISHRAE team for ISTL	Rewanth Punna, Saurab Rao, Tejas Mahadev, Karan Bhojwani	
alumni association Reconnect	Newbie Awards	Nipun Manghi	
	Sportsperson of the Year award	Sowmya Chillara	
	Award for Cultural Excellence	Vibha Ravan	
	Award for Creative Excellence	Suyash Jaju	
	Award for Excellence in Music	Anirudh Iyer	
		Sai Niranjan	
	Award for Technical Excellence	Venkatesh Arunachalam	
		Shantanu Ghar	
	Award for Academic Excellence	Rutuja Patil	
	The Most Enterprising Student Award	Arvind Thangaraj	
Chinmay Chandak			
GSTian of the Year award	Tejas Kulkarni		



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Awards given to Subject and Overall Toppers



GS Tian of the Year Award:





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2.2 Make up class


SIES GRADUATE SCHOOL OF TECHNOLOGY

Department of Electronics & Telecommunication

Second Half of 2019

Lecture Schedule for Direct Second Year students

TIME	22/8/2019	7/09/19	21/09/19	5/10/19	VENUE
9.00 to 10.00	CTN	CTN	EDC-I	AM-III	CR 403
10.00 to 11.00	EDC-I	CTN	EDC-I	EDC-I	CR 403
11.00 to 12.00	EIC	EIC	DSD	EDC-I	CR 403
12.00 to 1.00	AM-III	AM-III	DSD	EIC	CR 403
1.00 to 1.30	BREAK	BREAK	BREAK	BREAK	CR 403
1.30 to 2.30	DSD	AM-III	AM-III	CTN	CR 403
2.30 to 3.30	DSD	EDC-I	CTN	CTN	CR 403



Time table coordinator




Head of Department



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SIES Graduate School Of Technology, Nerul					
Mechanical Department					
Extra Lectures					
Time Table for Direct Second Year (DSE) Students					
SH2019					
					Date:22/08/2019
Saturday	9:00-10:00	10:00-11:00	11:00-12:00	12:00-1:00	2:00-3:00
9/7/2019	AMIII	SOM	TD	PPI	MT
	VPP	CDC	KVC	GSK	RSN
Saturday	9:00-10:00	10:00-11:00	11:00-12:00	12:00-1:00	2:00-3:00
9/21/2019	SOM	PPI	AMIII	MT	TD
	CDC	GSK	VPP	RSN	KVC
Saturday	9:00-10:00	10:00-11:00	11:00-12:00	12:00-1:00	2:00-3:00
10/5/2019	SOM	PPI	AMIII	MT	TD
	CDC	GSK	VPP	RSN	KVC
Saturday	9:00-10:00	10:00-11:00	11:00-12:00	12:00-1:00	2:00-3:00
10/19/2019	AMIII	SOM	TD	PPI	MT
	VPP	CDC	KVC	GSK	RSN


 Dr. R. Nehete
 HoD


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DSE S#19

SIES GRADUATE SCHOOL OF TECHNOLOGY
DEPARTMENT OF COMPUTER ENGINEERING
CLASS TIME TABLE (2nd Half of 2019)
(DIPLOMA)

Class: SE CE D

W.e.f: 19th July 2019 to 21 July 2

Class Incharge: Prof. Namrata Patel

Room No:L05

Day/Time	9.00 to 10.00	10.00 to 11.00	11.00 to 12.00	12.00 to 1.00		1.30 to 2.30	2.30 to 3.30	3.30 to 4.30	4.30 to 5.30
Monday	ECCF	DLDA	DS	DM		DL/D3/KNP/LAB 322		BE/D3/MBK/LAB 321	
	(MBK)	(KNP)	(DR)	(NP)					
					L				
Tuesday	DS	AM-III	AM-III	ECCF	U	DM	OOPM	OOPM/PST/LAB	
	(DR)	(AR)	(AR)	(MBK)	N	(NP)	(PST)		
					C				
Wednesday	DSL/D2/VGB/LAB5		ECCF	DS	H	AM-III	DLDA	DLDA	DM
			(MBK)	(DR)		(AR)	(KNP)	(KNP)	(NP)
					B				

Name of the Subject:

ECCF: Electronic Circuits and Fundamentals

OOPM : Object oriented Programming and Methodology

DS :Data Structures

DLDA : Digital logic and design analysis

DM : Discrete Mathematics

AM III : Applied Maths III

Name of the Faculty:


MBK : Prof. Madhuri Kulkarni

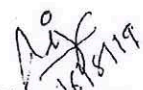
DR : Prof. Dipti Reddy


KNP : Prof. Kalyani Pampattiwar

NP: Prof. Namrata Patel

AR: Prof. Asha Raj


 Prof. Namrata Patel
 Class Incharge


 Dr. Rizwana Shaikh
 Head of the Department


 Dr. Atul Kemkar
 I/C Principal


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**SIES GRADUATE SCHOOL OF TECHNOLOGY
DEPARTMENT OF INFORMATION TECHNOLOGY**

SE IT DS~~E~~ Students extra lecture timetable (2nd Half of 2019)

Date: 19/08/2019

Sr. No.	Time	Subject
1	9.00 - 11.00	DBS
2	11.00 - 12	PC
3	12.00 - 1.00	JAVA
4	12.00 - 2.30	JAVA
5	2.30 - 4.30	LD

Date: 20/08/2019

Sr. No.	Time	Subject
1	9.00 - 11.00	JAVA
2	11.00 - 1.00	PC
3	1.30 - 3.30	MATHS
4	3.30 - 5.30	DSA

Date: 21/08/2019

Sr. No.	Time	Subject
1	9.00 - 10.00	PC
2	10.00 - 11	DSA
3	11.00 - 1.00	MATHS
4	12.00 - 2.30	DBS
5	2.30 - 4.30	LD

for books
Class Incharge


HEAD HOD(IT)
Department of Information Technology

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